

IN THE SPECIFICATION

Please amend the specification as follows:

AB 12/16/08
Page 14, ~~lines 8-23~~

The user can also create an album for reproducing (or writing, onto a CDR,) a freely selected combination of the audio data files stored under the disk directory. Although Fig. 4A shows each album as having a directory structure similar[[ly]] to the disks, the album is, in practice, a listing of data specifying a plurality of the audio data files (tracks) stored under the disk directory.

Page 14, line 24 – page 15, line 4:

When a desired disk or album is designated in the normal reproduction mode, the reproduction mode process sequentially reproduces the tracks contained in the disk or album (i.e., the tracks stored under the disk directory or specified by the listing of the album), after which the process is brought to [[en]] an end. Only one music piece can be reproduced, or a plurality of disks or albums can be successively reproduced in this reproduction mode as desired by the user.

Page 16, line 21 – page 17, line 5:

By keeping the erasure flag in the reset condition prior to the transfer process and then setting the file validity information to indicate “non-valid” after the transfer process as noted above, it is possible to disable any access to the actual data portion, other than the access for the data transfer purpose, without placing the actual data portion in the erased state. Thus, even when an unexpected accident, such as power failure or shutdown, has occurred the instant data transfer to the destination is completed, it is possible to reliably avoid a situation against the SCMS standard that the same actual data portion is left in both the destination storage medium

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and the source storage medium (hard disk 5).

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Please amend the specification as follows:

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Paragraph 0026:

FIG. 1 is a block diagram showing an exemplary general setup of the digital-audio-signal recording apparatus of the present invention, in which a controller 2 includes an ATAPI (acronym for At [[bus]] Attachment Packet Interface) interface 20, a subcode detection section 21, a digital audio interface 22, and an analog audio interface 23. To the controller 2 (ATAPI interface 20) are connected, via an ATAPI bus 11, a CD drive 4 and a hard disk drive (hereinafter simply referred to as a "hard disk") 5. Further, a FIFO memory 3 and a CPU 1 are connected to the controller 2 (ATAPI interface 20) via a memory bus 12 and a CPU bus 10, respectively. ROM 6 and a RAM 7, in addition to the controller 2, are connected to the CPU bus 10, and a user interface 8 is connected to the CPU 1.

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